

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	18613	ddr or (double adj5 data adj5 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:31
L2	6175	(sense adj3 amplifier\$2) and amplifier\$2 and multiplexer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:31
L3	4610	L2 and (switch\$2 or transistor) and (register or flip\$1flop\$2 or latch\$2 or delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:31
L4	378	L3 and L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:31
L5	70	L4 and 365/189.01,189.02,189.04,189.05,189.12,194.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:31
L6	1	((data adj23 circuit) and register and (selection adj2 switch\$2) and wires and length and (active adj2 region) and (first adj3 data adj3 group) and (second adj3 data adj3 group) and driver).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:34
L8	1	((data adj3 circuit) and register and (register adj3 selection adj3 switches) and (active adj2 region) and (first adj3 data adj3 group) and (second adj3 data adj3 group) and driver).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:35
L9	1	((data adj3 circuit) and register and (selection adj2 switches) and wires and lengths and (data adj2 group) and (second adj2 wires) and shorter and driver).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:37

S1	17376	ddr or (double adj5 data adj5 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:29
S2	45	S1 same multiplexer\$2 same (switch\$2 or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:20
S3	2455	(sense adj3 amplifier\$2) same amplifier\$2 same multiplexer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:32
S4	744	S3 same (switch\$2 or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:30
S5	237	S4 same (register or flip\$1flop\$2 or latch\$2 or delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21
S6	19	S5 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:31
S7	8	S5 same (output adj3 driver\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21
S8	5813	(sense adj3 amplifier\$2) and amplifier\$2 and multiplexer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:32

S9	4349	S8 and (switch\$2 or transistor) and (register or flip\$1flop\$2 or latch\$2 or delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:32
S10	329	S9 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21
S11	20	S10 and (output adj2 driver\$2) and pipe\$1lin\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:22
S12	207	S10 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:22
S14	16	((("5895482") or ("5978884") or ("6055210") or ("6154417") or ("6243797") or ("6288947") or ("6337930"))).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/28 13:35
S15	0	khang-chang.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:35
S16	22	khang-chang-\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:35
S17	0	kim-joung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:35

S18	70	kim-joung-\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:35
S19	14	S1 and S3 and S5 and S9 and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/28 13:36
S20	18613	ddr or (double adj5 data adj5 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:23
S21	45	S20 same multiplexer\$3 same (switch\$2 or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:20
S22	2622	(sense adj3 amplifier\$2) same amplifier\$2 same multiplexer\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21
S23	787	S22 same (switch\$2 or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21
S24	254	S23 same (register or flip\$1flop\$3 or latch\$2 or delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21
S25	254	S23 same (register or flip\$1flop\$2 or latch\$2 or delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21

S26	8	S25 same (output adj3 driver\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:21
S27	20	S25 and (output adj2 driver\$2) and pipe\$1lin\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:22
S29	189	S25 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:31